

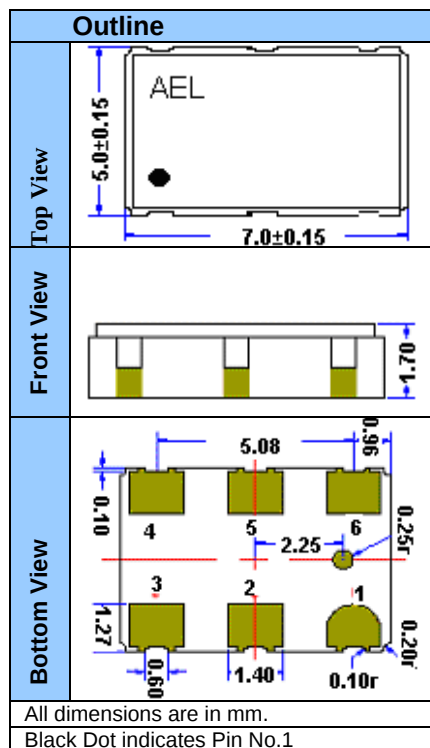
**Features**

- Package 7X5 mm Ceramic
- RoHS Compliant Standard
- LVDS Output
- Supply Voltage 3.3Volts
- Low Jitter

Table 1

Code	Stability Codes		F	G	M
	Frequency Stability		±20 PPM	±25 PPM	±50 PPM
E	Temperature Range	-10°C to +70°C			
I		-40°C to +85°C			

Denotes Available Denotes not Available

Mechanical Specification**Pin Configuration**

PIN	Description
1	No Connect
2	Output Enable/Disable
3	Ground
4	Output
5	Output*
6	Supply

XO 580

Frequency 10.9 – 670 MHz

Parameters	Conditions
Series:	XO 580
Frequency Range:	10.9 – 670 MHz
Supply Voltage:	3.3V ± 10%
Output Waveform:	LVDS
Output High Level:	1.60 Volts (max)
Output Low Level:	0.90 Volts (min)
Differential Output (Vod)	250 - 450 mV
Output Offset Voltage (Vos)	1.125 - 1.375 Volts
Differential Output Error(dVos):	50 mV (max)
Output Symmetry	47% - 53% (Referenced to 50% amplitude)
Jitter	0.6 pS RMS (max) 12KHz - 20MHz from Fnominal 2.8 pS RMS (max) 10 Hz - 20MHz from Fnominal
Rise / Fall time:	150 - 230 pS (Vth is 20% and 80% of waveform)
Output Short Circuit Current	-20 mA (max) Vout = 0.0 V
Vcc Supply Current:	80mA (max)
Enable/Disable Internal Pull-Up:	50Kohm (min) To Vcc (Equivalent Resistance)
Frequency stability:	(Table 1)Specify
Operating Temperature Range:	(Table 1)Specify
V Disable:	0.8Volts (max) Referenced to Ground
V Enable:	2.0Volts (min) Referenced to Ground
Output Leakage:	±20 uA (Vout = Vcc) pad1 low, device disabled ±20 uA (Vout = 0V) pad1 low, device disabled
Enable Time:	10nS (max) Time for output to reach a logic state
Disable Time:	10nS (max) Time for output to reach a high Zstate
Start up time:	5mS (max) Measured from the time Vcc=3.0V
Storage temperature range:	-55°C to +125° C

Specifications subject to change without notice
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ANDHRA ELECTRONICS LIMITED

34 & 35, IDA, Kakinada, AP – 533 005,
INDIA.

URL: www.andhraelec.com

Phone : +91-884-2342203

Fax : +91-884-2341145

E-mail: info@andhraelec.com



LOAD CIRCUIT



The diagram illustrates the system architecture with the following components and connections:

- Series**: Connected to **OTR** and **Stability**.
- OTR**: Connected to **Stability** and **Frequency 670MHz**.
- Stability**: Connected to **Supply Voltage** and **Output LVDS**.
- Supply Voltage**: Connected to **Output LVDS**.
- Frequency 670MHz**: Connected to **Output LVDS**.

Phone : +91-884-2342203
Fax : +91-884-2341145
E-mail: info@andhraelec.com

